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3/31/03
C.P.

First Named Inventor	Frankie F. Roohparvar	SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
Serial No.	09/608,580	
Filing Date	June 30, 2000	
Group Art Unit	2818	
Examiner Name	Trong Q. Phan	
Confirmation No.	9345	
Attorney Docket No.	400.006US01	
Title: ZERO LATENCY-ZERO BUS TURNAROUND SYNCHRONOUS FLASH MEMORY		

Commissioner for Patents
Box AF
Washington, D.C. 20231

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In compliance with 37 C.F.R. §§ 1.56 and 1.97, *et seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified Application. Applicant respectfully requests that this Supplemental Information Disclosure Statement be entered and the reference listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to MPEP §609, Applicant further requests that the Examiner initial next to each reference on the Form 1449 to indicate that the listed references have been considered. Applicant further requests that a copy of the initialed Form 1449 be returned with the next official communication.

As an Office Action has issued in this application, Applicant encloses a check in the amount of \$180.00. However, the Commission for Patents is hereby authorized to charge any additional fees or credit overpayments to Deposit Account No. 501373. If the Examiner has any questions or concerns regarding this application, please contact the undersigned at the number listed below.

Respectfully submitted,

Date: 3/24/03

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